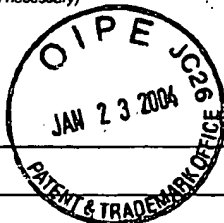


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Application Number	09/135,413
Filing Date	August 14, 1998
First Named Inventor	Forbes, Leonard
Group Art Unit	2818
Examiner Name	Nguyen, Viet

Sheet 1 of 1

Attorney Docket No: 303.354US2

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W	US-4,980,303	12/25/1990	Yamauchi, T.	437	31	08/18/1988
W	US-4,994,401	02/19/1991	Ukai, Y.	437	40	03/26/1990
W	US-5,189,504	02/23/1993	Nakayama, S. , et al.	257	422	01/30/1992
W	US-5,336,361	08/09/1994	Tamura, A. , et al.	438	767	11/02/1992
W	US-5,367,306	11/22/1994	Hollon, , et al.	342	386	06/04/1993
W	US-5,409,501	04/25/1995	Zauns-Huber, R. , et al.	8	94.29	07/06/1992
W	US-5,990,531	11/23/1999	Taskar, N. R., et al.	257	410	11/12/1997
W	US-6,100,193	08/08/2000	Suehiro, S. , et al.	438	685	09/24/1997
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W	US-6,365,919	04/02/2002	Tihanyi, J. , et al.	257	77	07/11/2000

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Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
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V. Nguyen

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W		WOLF, S., Silicon Processing for the VLSI Era, Vol. 3, Lattice Press, Sunset Beach, CA, (1995), 311-312	

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*V. Nguyen*

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Application Number	09/135413
Filing Date	August 14, 1998
First Named Inventor	Forbes, Leonard
Group Art Unit	2818
Examiner Name	Nguyen, Viet

Sheet 1 of 1

Attorney Docket No: 303.354US2

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DATE CONSIDERED

9/16/04

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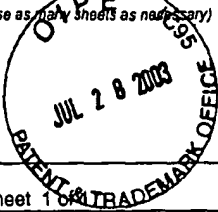
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